

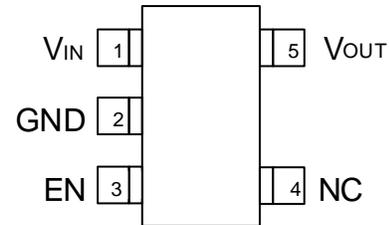
Description

The AP7354 is a low dropout regulator with high output voltage accuracy. The AP7354 includes a voltage reference, error amplifier, current limit circuit and an enable input to turn it on/off. With the integrated resistor network, fixed output voltage versions can be delivered.

With its ultra-low quiescent current, the AP7354 is well suited for low-power handheld, wearable devices, and other battery-operated devices requiring an extended time period until new battery replacement.

The AP7354 is available in the SOT25 packages

Pad Assignments



SOT25

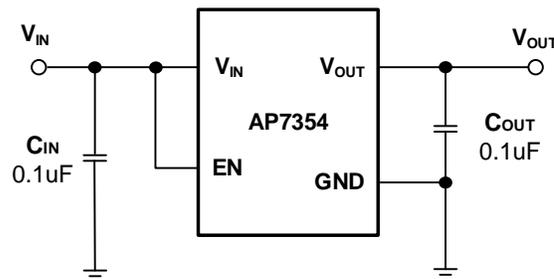
Features

- Wide V_{IN} Range: 2.0 V to 5.5
- Guarantee Output Current, 150mA
- Output Voltage Range: 1.2 to 4.5V
- V_{OUT} Accuracy: $\pm 1\%$
- Quiescent Current as Low as 0.25 μ A
- Output Discharge option available
- Typical Standby Current 0.02 μ A
- ESD Protection Exceeds JESD 22
 - Exceeds 4000-V Human Body Model (A114)
 - Exceeds 400-V Machine Model (A115)
- Latch-Up Exceeds 400mA per JESD 78, Class I
- **Totally Lead-Free & and Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Applications

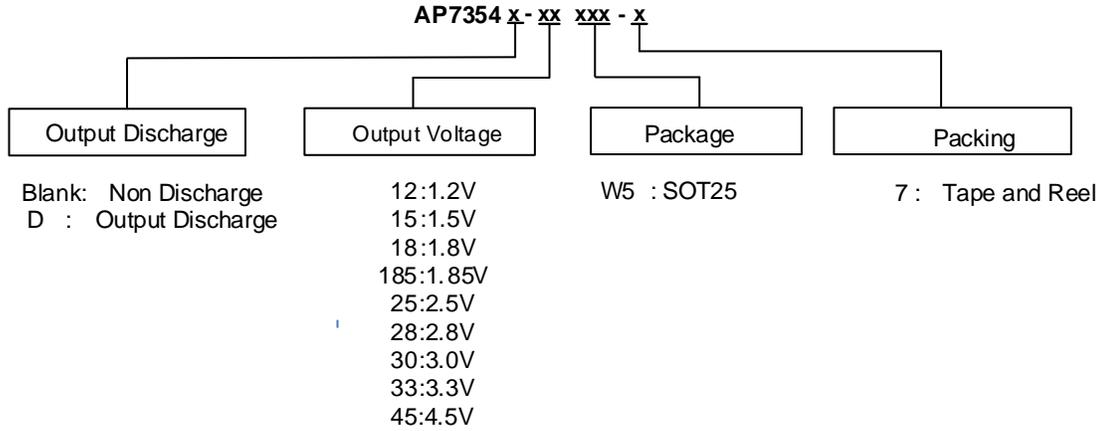
- Wearable Electronics
- Sensor Module for Internet-of-Things (IoT)
- Wireless Communication Module
- Battery-Operated Device
- Camera
- Image Sensor

Typical Applications Circuit (Note 4)



- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green", and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. X5R- and X7R-type capacitors are suggested due to their minimal variation in value and ESR over temperature.

Ordering Information

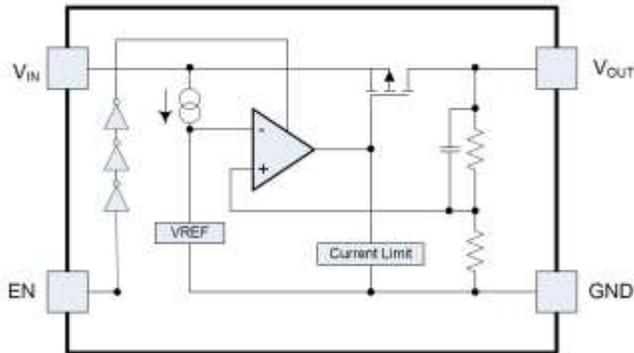


Output Voltage	SOT25 Without Discharge	SOT25 With Discharge	7" Tape and Reel	
			Quantity	Part Number Suffix
1.2	AP7354-12W5-7	AP7354D-12W5-7	3,000/Tape & Reel	-7
1.5	AP7354-15W5-7	AP7354D-15W5-7	3,000/Tape & Reel	-7
1.8	AP7354-18W5-7	AP7354D-18W5-7	3,000/Tape & Reel	-7
1.85	AP7354-185W5-7	AP7354D-185W5-7	3,000/Tape & Reel	-7
2.5	AP7354-25W5-7	AP7354D-25W5-7	3,000/Tape & Reel	-7
2.8	AP7354-28W5-7	AP7354D-28W5-7	3,000/Tape & Reel	-7
3.0	AP7354-30W5-7	AP7354D-30W5-7	3,000/Tape & Reel	-7
3.3	AP7354-33W5-7	AP7354D-33W5-7	3,000/Tape & Reel	-7
4.5	AP7354-45W5-7	AP7354D-45W5-7	3,000/Tape & Reel	-7

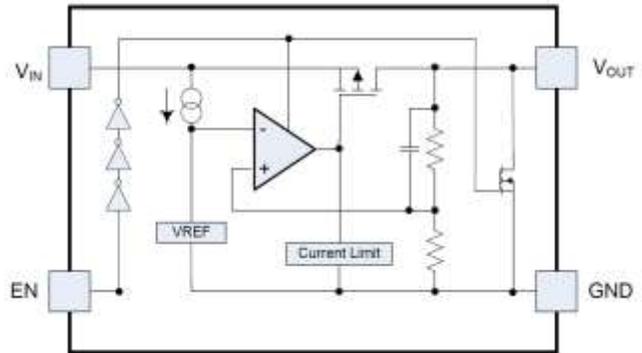
Pad Description

Pin Number SOT25		Function
3	EN	Chip enable: This should be driven either high or low and must not be floating. Driving EN high enables regulator output, while pulling it low places regulator into shutdown mode.
2	GND	Ground
5	V _{OUT}	Regulated Output Voltage
1	V _{IN}	Power input
4	NC	No connect

Functional Block Diagram



AP7354 (without discharge)



AP7354D (with discharge)

Absolute Maximum Ratings (Note 5)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Model ESD Protection	4	kV
ESD MM	Machine Model ESD Protection	400	V
V_{IN}	Input Voltage	6.0	V
V_{EN}	Input Voltage at EN pad	6.0	V
V_{OUT}	Output Voltage to GND	-0.3 to $V_{IN} + 0.3$	V
T_A	Operating Ambient Temperature	-40 to +85	°C
T_J	Maximum Junction Temperature	+125	°C
T_{STG}	Storage Temperature	-55 to +125	°C
P_D	Power Dissipation (Note 6)	315	mW

- Notes:
- Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
 - This is based on an application temperature of 40°C. Derate 3.75 mW per °C for each degree above 40°C.

Recommended Operating Conditions

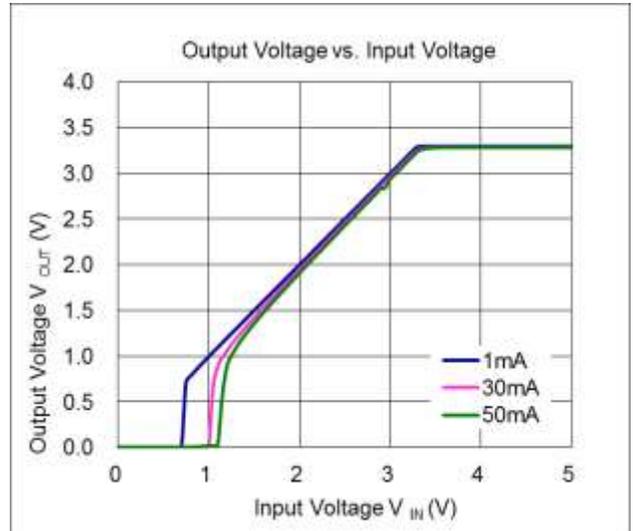
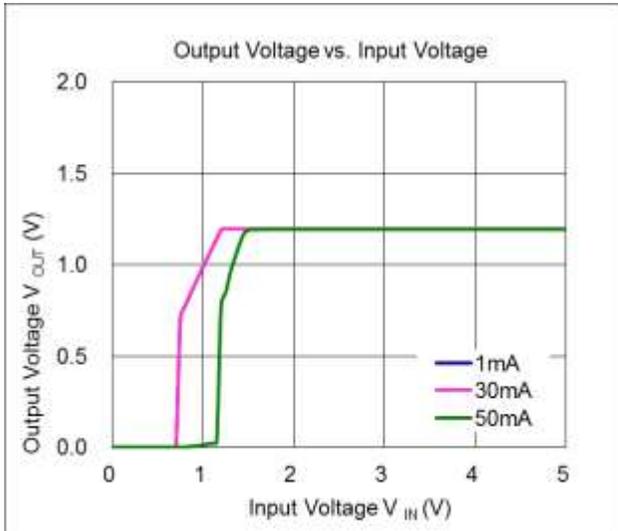
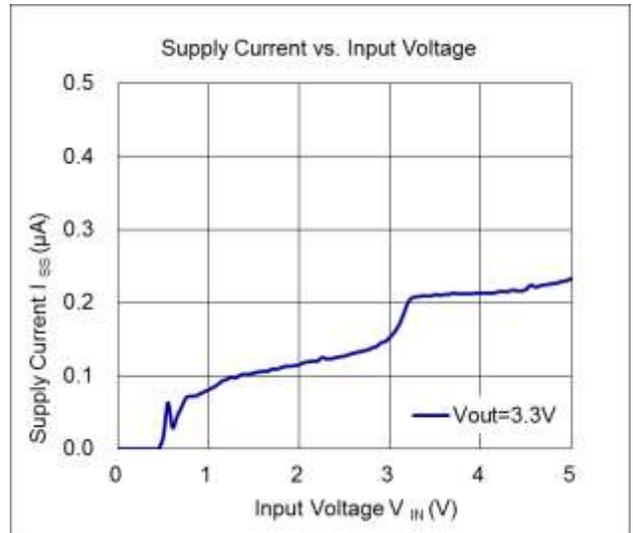
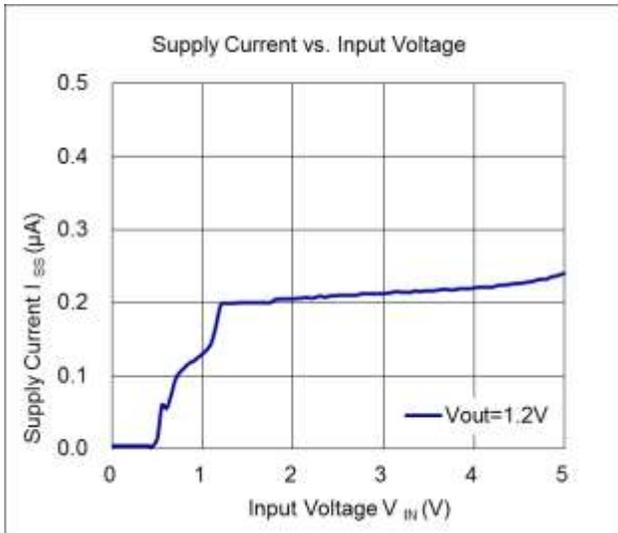
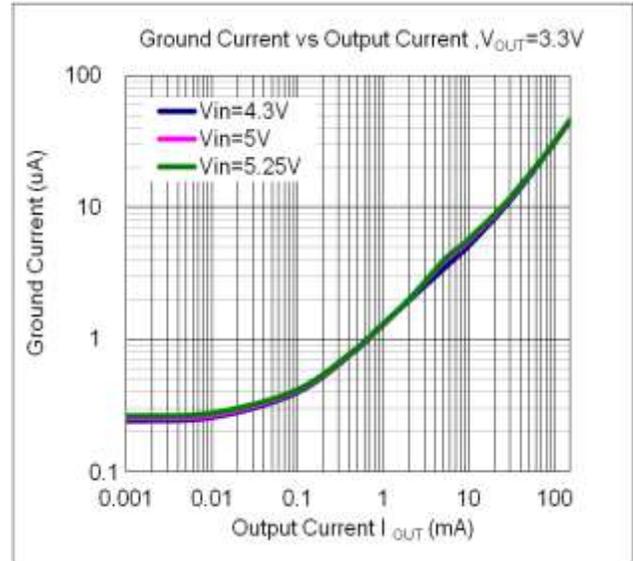
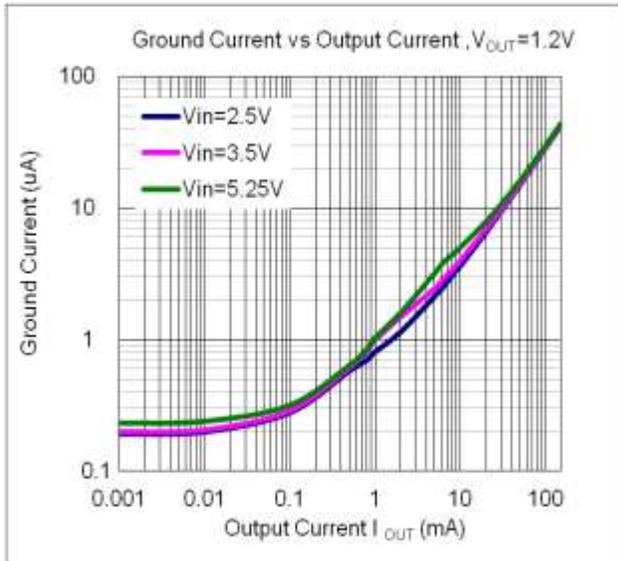
Symbol	Parameter	Min	Max	Unit
V_{IN}	Input Voltage	2.0	5.5	V
I_{OUT}	Output Current	0	150	mA
T_A	Operating Ambient Temperature	-40	+85	°C

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$) $V_{EN} = V_{IN} = V_{OUT} + 1\text{V}$ ($1.5\text{V} < V_{OUT} \leq 4.5\text{V}$), $V_{EN} = V_{IN} = 2.5\text{V}$ ($V_{OUT} \leq 1.5\text{V}$), $I_{OUT} = 1\text{mA}$, $C_{IN} = C_{OUT} = 0.1\mu\text{F}$, unless otherwise specified.)

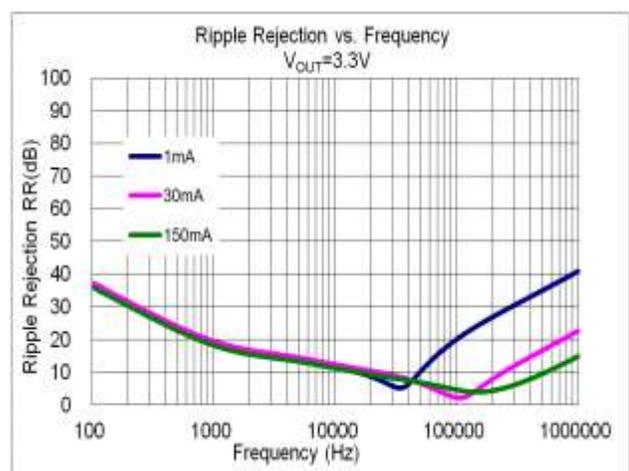
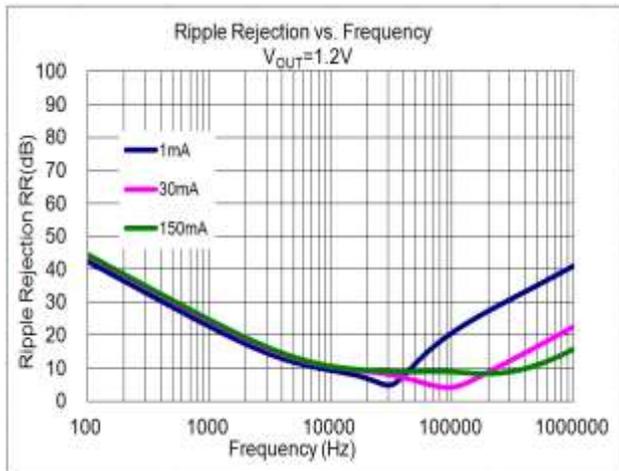
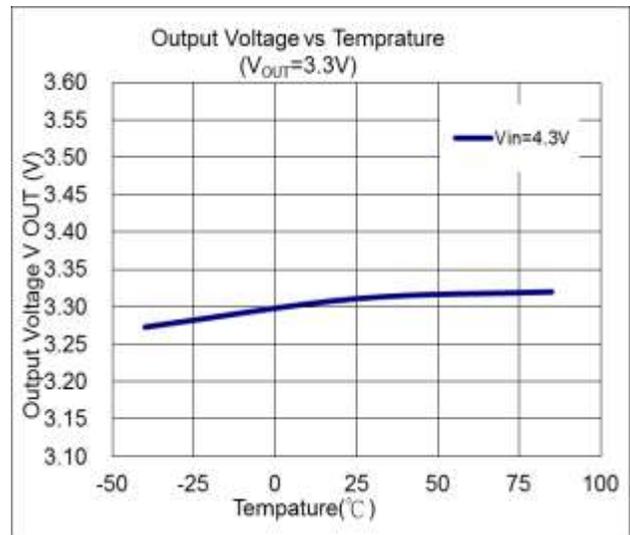
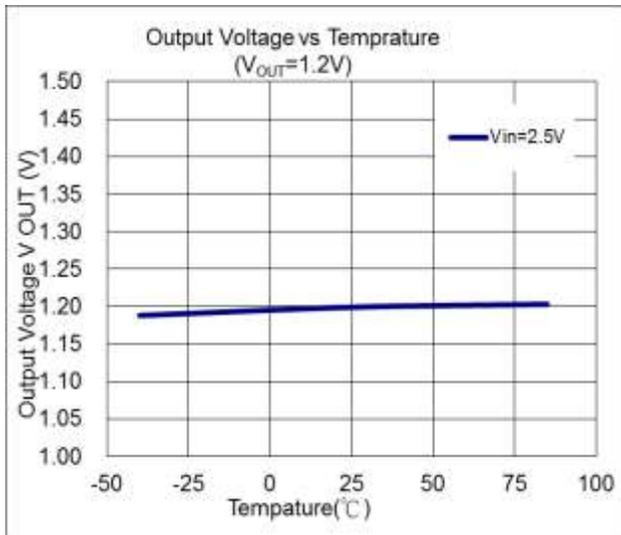
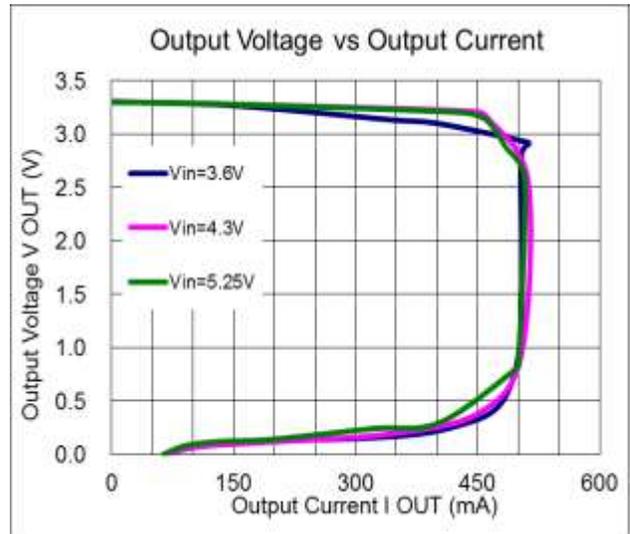
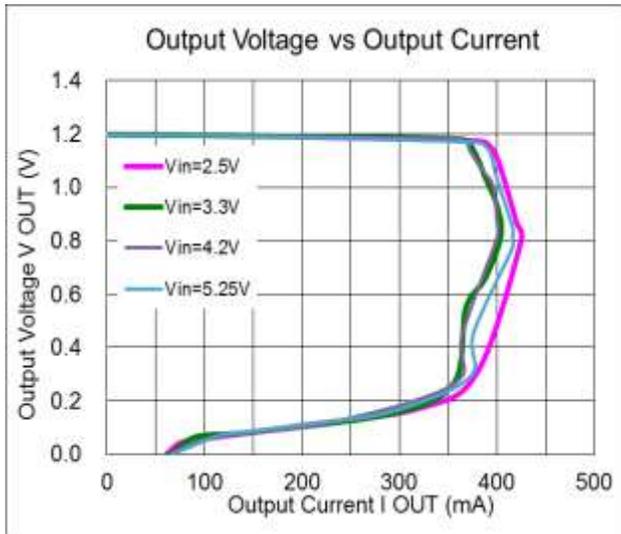
Parameter	Conditions		Min	Typ	Max	Unit
Input Voltage	$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$		2.0	—	5.5	V
Output Voltage Accuracy	$V_{OUT} > 2.0\text{V}$ $I_{OUT} = 1\text{mA}$	$T_A = +25^\circ\text{C}$	-1	—	+1	%
		$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	-2	—	+2	
	$V_{OUT} \leq 2.0\text{V}$ $I_{OUT} = 1\text{mA}$	$T_A = +25^\circ\text{C}$	-40	—	40	mV
		$T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$	-80	—	80	
Line Regulation ($\Delta V_{OUT}/\Delta V_{IN}/V_{OUT}$)	MAX ($V_{OUT} + 1.0\text{V}$, 2.5V) $\leq V_{IN} \leq 5.5\text{V}$ (all versions except 4.5V)		—	0.02	0.1	%/V
Load Regulation (ΔV_{OUT})	$1\text{mA} \leq I_{OUT} \leq 150\text{mA}$ (all versions except 4.5V)		-40	—	40	mV
	$1\text{mA} \leq I_{OUT} \leq 150\text{mA}$ (applicable to 4.5V version)		-55	—	55	mV
Short Circuit Current Limit (Note 7)	$V_{OUT} = 0\text{V}$		—	60	—	mA
Quiescent Current (Note 8)	$I_{OUT} = 0\text{mA}$		—	0.25	0.6	μA
I _{STANDBY}	Set EN low, No load		—	0.02	0.2	μA
Output Current	—		150	—	—	mA
Dropout Voltage (Note 9)	$I_{OUT} = 150\text{mA}$	$V_{OUT} = 1.2\text{V}$	—	0.60	0.90	V
		$V_{OUT} = 1.5\text{V}$	—	0.43	0.75	
		$V_{OUT} = 1.8\text{V}$	—	0.33	0.60	
		$V_{OUT} = 1.85\text{V}$	—	0.32	0.58	
		$V_{OUT} = 2.5\text{V}$	—	0.22	0.48	
		$V_{OUT} = 2.8\text{V}$	—	0.19	0.40	
		$V_{OUT} = 3.0\text{V}$	—	0.18	0.35	
		$V_{OUT} = 3.3\text{V}$	—	0.16	0.35	
		$V_{OUT} = 4.5\text{V}$	—	0.14	0.35	
EN Input Low Voltage	—		—	—	0.4	V
EN Input High Voltage	—		1.0	—	5.5	V
Active Output Discharge Resistance (Note 10)	$V_{IN} = 4.0\text{V}$, $V_{EN} = 0\text{V}$		—	35	—	Ω
θ_{JA} Thermal Resistance Junction-to-Ambient	Note 11	SOT25	—	162	—	$^\circ\text{C}/\text{W}$
θ_{JC} Thermal Resistance Junction-to-Case	Note 11	SOT25	—	38	—	$^\circ\text{C}/\text{W}$

- Notes:
7. Short circuit current is measured with V_{OUT} pulled to GND.
 8. Quiescent current defined here is the difference in current between the input and the output.
 - 9 Dropout voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value.
 10. AP7354 is available with 2 options: built-in discharge (AP7354D) and non-discharge (AP7354).
 11. Test condition: Device mounted on 1"x1" FR-4 MRP substrate PCB, 2oz copper, with minimum recommended pad layout..

Performance Characteristics



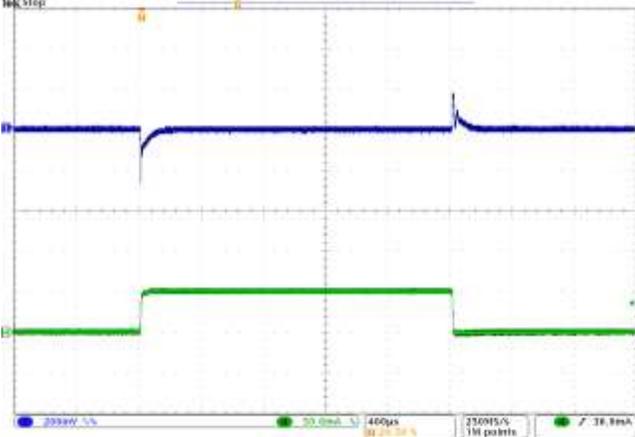
Performance Characteristics



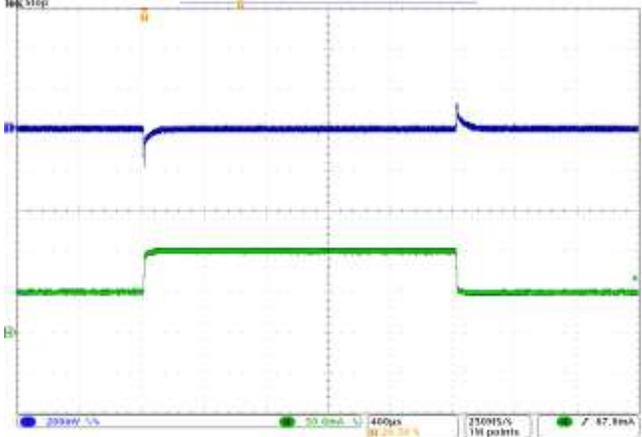
Performance Characteristics

Load Transient Response ($C_{IN} = C_{OUT} = 0.1\mu F$, $T_R = T_F = 5.0\mu s$, unless otherwise specified)

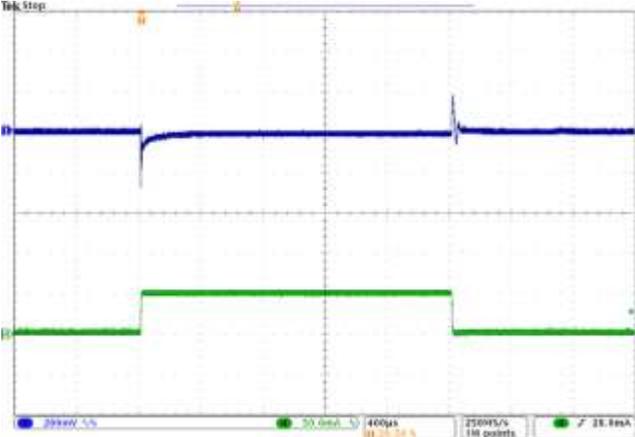
$V_{IN} = 3.3V$, $V_{OUT} = 1.2V$, $I_{OUT} = 1\sim 50mA$



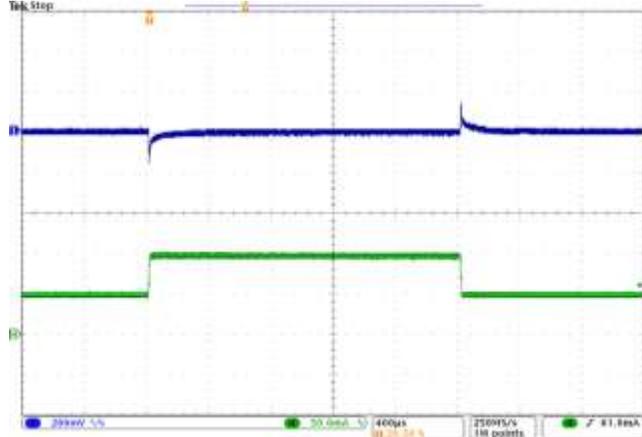
$V_{IN} = 3.3V$, $V_{OUT} = 1.2V$, $I_{OUT} = 50\sim 100mA$



$V_{IN} = 4.3V$, $V_{OUT} = 3.3V$, $I_{OUT} = 1\sim 50mA$



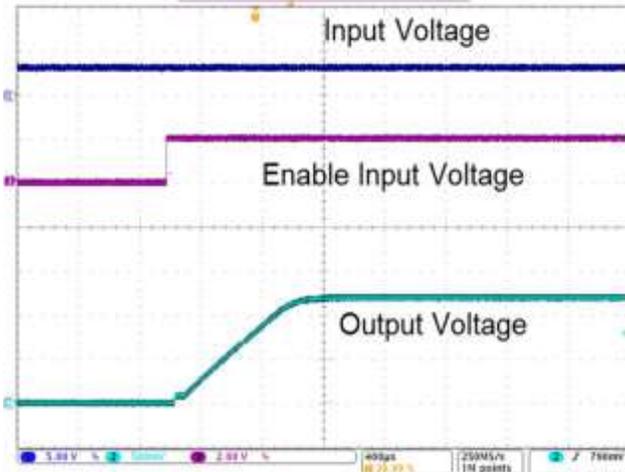
$V_{IN} = 4.3V$, $V_{OUT} = 3.3V$, $I_{OUT} = 50\sim 100mA$



Performance Characteristics

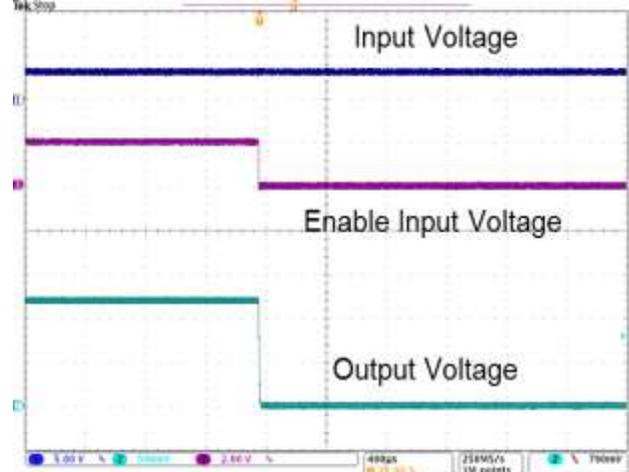
TURN ON

$V_{IN} = 3.3V, V_{OUT} = 1.2V, I_{OUT} = 0mA, C_{IN} = C_{OUT} = 0.1\mu F$

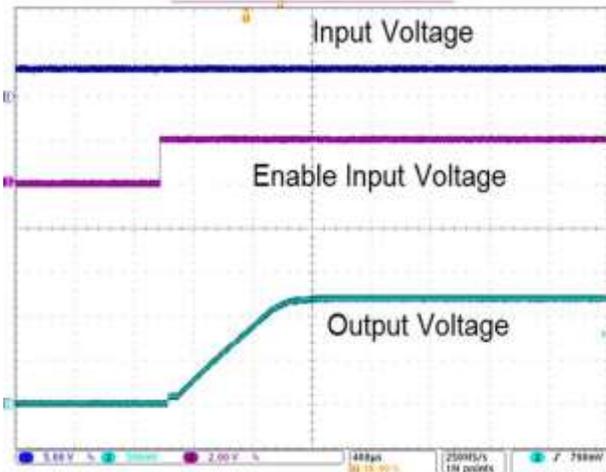


TURN OFF

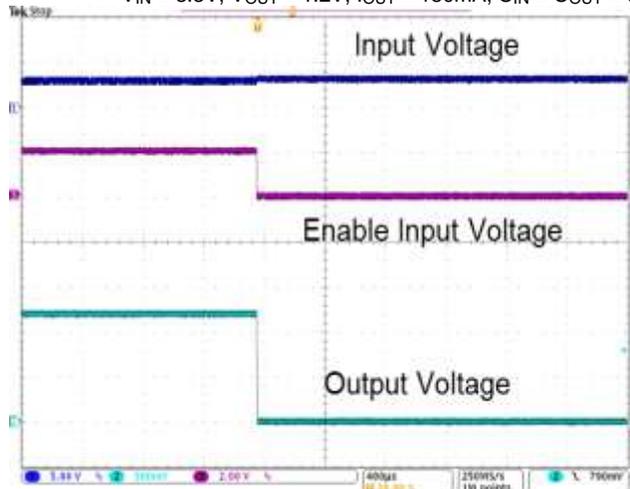
$V_{IN} = 3.3V, V_{OUT} = 1.2V, I_{OUT} = 0mA, C_{IN} = C_{OUT} = 0.1\mu F$



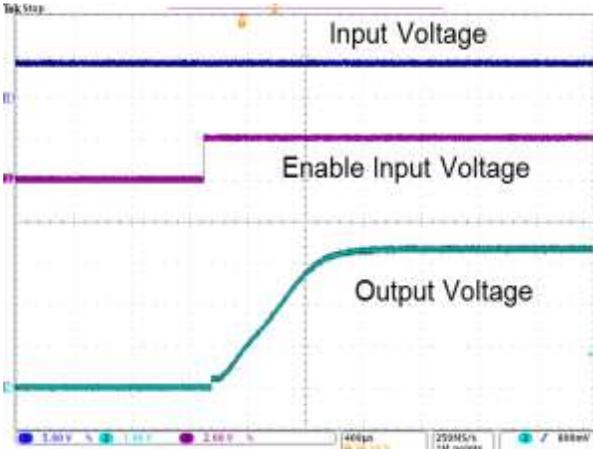
$V_{IN} = 3.3V, V_{OUT} = 1.2V, I_{OUT} = 150mA, C_{IN} = C_{OUT} = 0.1\mu F$



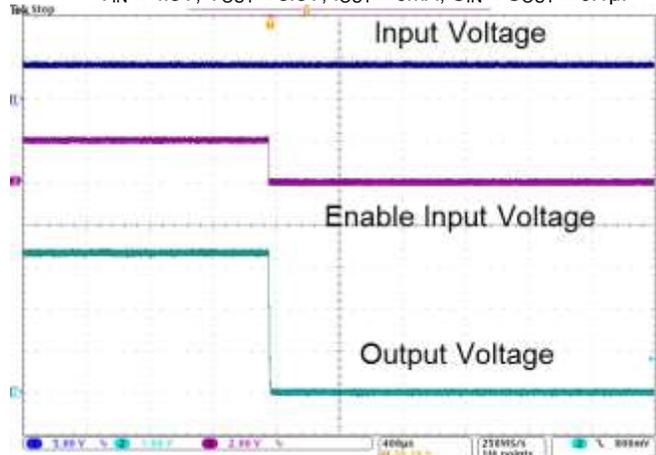
$V_{IN} = 3.3V, V_{OUT} = 1.2V, I_{OUT} = 150mA, C_{IN} = C_{OUT} = 0.1\mu F$



$V_{IN} = 4.3V, V_{OUT} = 3.3V, I_{OUT} = 0mA, C_{IN} = C_{OUT} = 0.1\mu F$



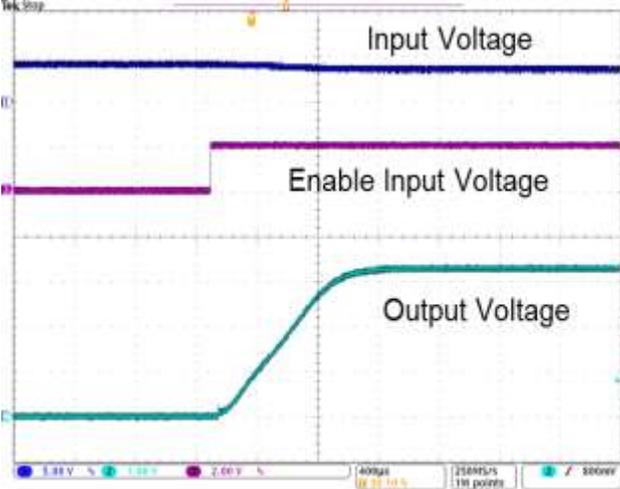
$V_{IN} = 4.3V, V_{OUT} = 3.3V, I_{OUT} = 0mA, C_{IN} = C_{OUT} = 0.1\mu F$



Performance Characteristics

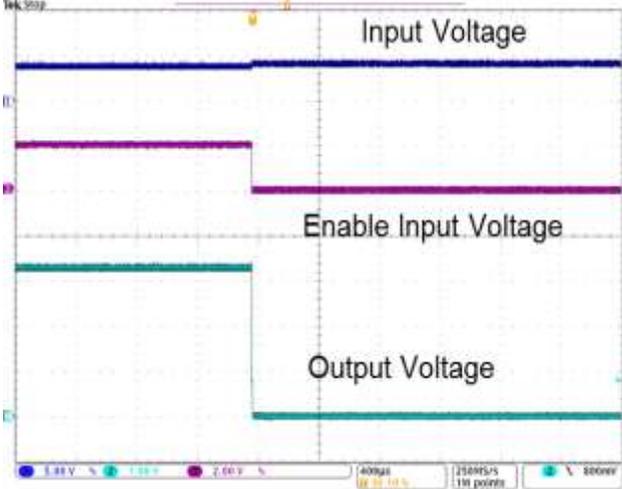
TURN ON

$V_{IN} = 4.3V$, $V_{OUT} = 3.3V$, $I_{OUT} = 150mA$, $C_{IN} = C_{OUT} = 0.1\mu F$



TURN OFF

$V_{IN} = 4.3V$, $V_{OUT} = 3.3V$, $I_{OUT} = 150mA$, $C_{IN} = C_{OUT} = 0.1\mu F$



Application Information

Output Capacitor

An output capacitor (C_{OUT}) is needed to improve transient response and maintain stability. The AP7354 is stable with very small ceramic output capacitors. The ESR (Equivalent Series Resistance) and capacitance drive the selection. If the application has large load variations, it is recommended to utilize low-ESR bulk capacitors. It is recommended to place ceramic capacitors as close as possible to the load and the GND pad and care should be taken to reduce the impedance in the layout.

Input Capacitor

To prevent the input voltage from dropping during load steps, it is recommended to utilize an input capacitor (C_{IN}). A minimum 0.1 μ F ceramic capacitor is recommended between V_{IN} and GND pad to decouple input power supply glitch. This input capacitor must be located as close as possible to the device to assure input stability and reduce noise. For PCB layout, a wide copper trace is required for both V_{IN} and GND pad.

Enable Control

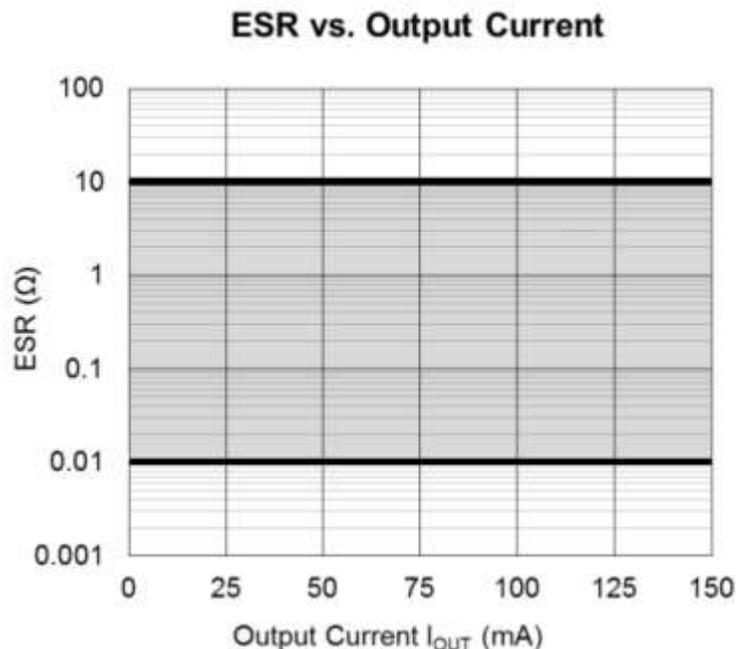
The AP7354 is turned on by setting the EN pad high, and is turned off by pulling them low. If this feature is not used, the EN pad should be tied to V_{IN} pad to keep the regulator output on at all time. To ensure proper operation, the signal source used to drive the EN pad must be able to swing above and below the specified turn-on/off voltage thresholds listed in the Electrical Characteristics section.

Layout Considerations

For good ground loop and stability, the input and output capacitors should be located close to the input, output, and GND pad of the device. The regulator GND pad should be connected to the external circuit ground to reduce voltage drop caused by trace impedance. Ground plane is generally used to reduce trace impedance. Wide trace should be used for large current paths from V_{IN} to V_{OUT} , and load circuit.

ESR vs. Output Current

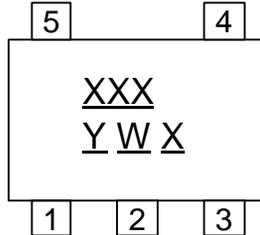
A ceramic type output capacitor is recommended for this series; however, the other output capacitors with low ESR also can be used. The relations between I_{OUT} (Output Current) and ESR of an output capacitor are shown below. The stable region is marked as the hatched area in the graph. Measurement conditions: Frequency Band: 10Hz to 2MHz, Temperature: -40°C to +85°C.



Marking Information

(1) SOT25

(Top View)



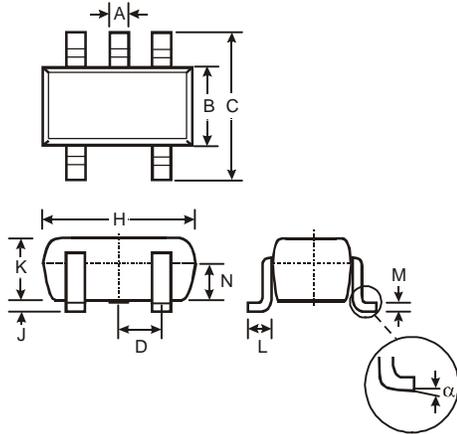
XXX : Identification Code
Y : Year 0 to 9
W : Week : A to Z : 1 to 26 week;
a to z : 27 to 52 week; z represents
52 and 53 week
X : Internal Code

Part Number	Package	Identification Code
AP7354-12W5-7	SOT25	A8A
AP7354-15W5-7	SOT25	A8B
AP7354-18W5-7	SOT25	A8C
AP7354-185W5-7	SOT25	A8D
AP7354-25W5-7	SOT25	A8E
AP7354-28W5-7	SOT25	A8F
AP7354-30W5-7	SOT25	A8G
AP7354-33W5-7	SOT25	A8H
AP7354-45W5-7	SOT25	A8J
AP7354D-12W5-7	SOT25	A9A
AP7354D-15W5-7	SOT25	A9B
AP7354D-18W5-7	SOT25	A9C
AP7354D-185W5-7	SOT25	A9D
AP7354D-25W5-7	SOT25	A9E
AP7354D-28W5-7	SOT25	A9F
AP7354D-30W5-7	SOT25	A9G
AP7354D-33W5-7	SOT25	A9H
AP7354D-45W5-7	SOT25	A9J

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT25

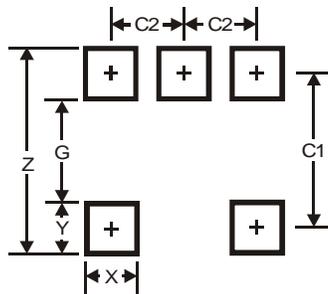


SOT25			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT25



Dimensions	Value
Z	3.20
G	1.60
X	0.55
Y	0.80
C1	2.40
C2	0.95

- Note:** The suggested land pattern dimensions have been provided for reference only, as actual pad layouts may vary depending on application. These dimensions may be modified based on user equipment capability or fabrication criteria. A more robust pattern may be desired for wave soldering and is calculated by adding 0.2 mm to the 'Z' dimension. For further information, please reference document IPC-7351A, Naming Convention for Standard SMT Land Patterns, and for International grid details, please see document IEC, Publication 97.
- Note:** For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.

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